

**Amendments to the Specification:**

Please delete the brief description of the drawings and substitute therefore --

**BRIEF DESCRIPTION OF THE DRAWINGS**

FIG. 1 is a block diagram showing the construction of a dot matrix display for a dynamic drive system according to one embodiment of this invention.

FIG. 2 is a schematic showing the construction of the essential elements in the display of the embodiment.

FIG. 2A is a schematic drawing of an alternate embodiment of the circuit shown in Figure 2.

FIG. 3 shows the timing for the essential signals in the display of the embodiment.

FIG. 4 is a schematic showing the construction of an erroneous lighting cancel circuit according to one modified example of the embodiment.

FIG. 5 is a schematic showing the construction of the essential elements in a display of another embodiment.

FIG. 6 is a schematic showing the construction of a buffer circuit that is used in the embodiment of FIG. 5.

FIG. 7 is a schematic showing the basic construction of a dot matrix display.

FIG. 8 is a circuit diagram of a model for the purpose of explaining the problem points of the prior art.--